

In the United States Patent and Trademark Office

Serial Number: 10/727238
Application. Filed: December 2, 2003
Applicant: Gary Shipton
Application Title: AN INTEGRATED CIRCUIT WHICH DISABLES WRITING CIRCUITRY
TO MEMORY WHEN THE POWER DROPS BELOW A POWER
THRESHOLD PREDETERMINED AND CONTROLLED BY THE
PROCESSOR
Examiner/GAU: Hari Patel 2115
Dated July 24, 2007
At: Balmain, NSW
Docket No. PEA12US

REPLY TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Commissioner for Patents
Washington, District of Columbia 20231


Dear Sir:

In reply to the Notice to File Corrected Application Papers mailed July 3, 2007, the applicant now encloses the required amended drawings pages 237 and 331, each marked "Replacement sheet", with page 237 including Fig. 278 and page 331 omitting Fig. 413.

The Applicant submits that all informalities for this application have now been corrected.

Very respectfully,

Applicant:



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